

Abstract

In a circuit arrangement for adapting the characteristic wave impedance at line ends of a vehicle data bus having at least two data bus lines and data terminals, each with a transceiver unit for differential-mode signal transmission on the data bus, the data bus is connected to the transceiver unit via a connection interface that includes a reactor for filtering interference on the data bus and an adaptation resistor network for adapting the characteristic wave impedance of a data bus line. The adaptation resistor network is arranged between the reactors and the respective line ends of the data bus lines, while the adaptation resistor network connects the line ends to ground via a capacitor.